EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
81	8	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receivs\$6) and associat\$6 and (receivs\$6) and time same reference\$6 same signal\$6) and high\$6 same dididididididididididididididididididi	USPAT	OR	OFF	2008/02/27 20:13
S2	14	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (ireceiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same same same same same same same same	US-PGPUB; USPAT; FPRS; EPC; JPO; DEBWENT; IBM_TDB	OR	OFF	2008/02/27

	***************************************	same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency)				
S	14	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same data and (analog same image\$6 same data and (receiv\\$6) and associat\\$6 and time same reference\\$6 and time same frequency same firequency same digital same convert\\$6 and (analog same to same digital same convert\\$6 or ADC) and (sampl\\$6 and equal \\$6 amd equal \\$6 am	US-PGPUB; USPAT; FFFRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:18
S4	4	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same frequency same	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2008/02/27

	circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase same differen\$6				
6	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and (receiv\$6) and associat\$6 and time same reference\$6 same signal\$6) and high\$6 same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same digital same convert\$6 or ADC) and same digital same convert\$6 or ADC) and same digital same convert\$6 or ADC) and correct\$6 same pixeuency) and correct\$6 same pixeuency]	US-PGPUB; USPAT; FPPS; EPC; JPC; DEFWENT; IBM_TDB	OR	OFF	2008/02/27 20:19

\$6	2	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$8 same differen\$6 and long\$6 same term\$6 same drift\$800.	US-PGPUB; USPAT; FPFS; EPC; JPC; DERWENT; IBM_TDB	OR	OFF	2008/02/27 20:22
S7	3	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive\$6) and associat\$6 and (ireceiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same frequency same frequency same signal\$6) and high\$6 same signal\$6) and high\$6 same frequency same same frequency same frequency same same frequency same same frequency same same same same same same same same	USPAT	OR	OFF	2008/02/27 20:36

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 same phase\$6 same differen\$6				
\$8	7	circuit\$6 and digital same display\$6 and generat\$6 and plural\$8 same data and (analog same image\$6 same data and (receiv\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 and high\$6 same frequency) and correct\$6 and phase\$6 same frequency\$6 same differen\$6	USPAT	OR .	OFF	2008/02/27 20:37

	5	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same images\$6 same data and (receiv\$\$6) and associat\$6 and (receiv\$\$6) and high\$6 same frequency\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 and high\$6 same frequency) and correct\$6 and play\$6 same (PLL or phase same lock\$6 and phase\$6 same firequen\$6 and (PLL or phase same lock\$6 sa	USPAT	OR	OFF	2008/02/27 20:37
S10	3	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receivs\$6) and associat\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same recircuit\$6 and tand time same reference\$1 same signal\$6) and high\$6 same frequency same same frequency same same same same same same same same	USPAT	OR	OFF	2008/02/27 20:38

		same digital same convert\$6 or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same look \$6 same look\$6 and feedback\$6				
\$11	2	circuit\$6 and digital same display\$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same images) same images same images same images and (receiv\$6) and associat\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same ingular same convert\$6 or ADC) and (sampl\$6 same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 and equal\$6 and high\$6 same frequency) and correct\$6 and equal\$6 and phase\$6 same look\$5 same look\$5 same look\$6 and (PLL or phase same lock\$6 and chase same lock\$6 and chase same look\$6 and dedack\$6 and drift\$6	USPAT	OR .	OFF	2008/02/27 20:38

S12	5	circuit\$6 and digital adj display \$6 and generat\$6 and plural\$6 same pixel\$1 same data and (analog same image\$6 same pixel\$1 same data and receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 and equal \$6 and high\$6 same clock\$6 and equal \$6 and figh\$8 same frequency) and correct\$6 and (PLL or phase\$6 and (PLL or phase same lock\$6 same lock\$6 same plock\$6 and plock\$6 same lock\$6 same lock\$6 same plock\$6 same	USPAT	OA	OFF	2008/02/27 20:45
S13	4	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data and receive \$6) and associat \$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6	USPAT	OR .	OFF	2008/02/27 20:46

	<i>положения в в в в в в в в в в в в в в в в в в в</i>	or ADC) and (sampli\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)				
S14	3	circuit\$6 and digital adj display \$\$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same data same receive \$6) and associat \$\$6 and (receiv\$6 and time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and circuit\$8 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock\$6 same lock\$6 same lock\$6 same lock\$6 same loch\$6 same loch\$6]	USPAT	OR .	OFF	2008/02/27 20:46

S15	3	circuit\$6 and digital adj display \$6 and generat\$6 same pural\$6 same pixel\$1 same data and (analog same imaqe\$6 same pixel\$1 same data same receive \$6) and associat \$6 and (receiv\$6 and same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 same frequency) and correct\$6 same differen\$6 and phase\$6 same differen\$6 and (PLL or phase same look\$6 same loop\$6)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:11
S16	2	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pixel\$1 same data and (analog same image\$6 same same receive \$6) and associat \$6 and (receiv\$6 same same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and candog same to same to same digital same convert\$6	USPAT	OR	OFF	2008/02/27 21:12

		or ADC) and (sampl\$6 same clock\$6 and equal \$6 and high\$6 same frequency) and correct\$6 and phase\$6 same differen\$6 and (PLL or phase same lock \$6 same loop\$6)				
S17	2	circuit\$6 and digital adj display \$6 and generat\$6 same plural\$6 same pival\$1 same data and (analog same image\$6 same adata same receive \$5) and associat \$6 and (receiv\$6 same time same reference\$6 same signal\$6) and high\$6 same frequency same circuit\$6 and (analog same to same digital same convert\$6 or ADC) and (sampl\$6 same frequency) and correct\$6 and (fanalog same convert\$6 or ADC) and sampl\$6 same differen\$6 and (PLL or phase same lock \$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DETWIENT; IBM_TDB	OR	OFF	2008/02/27
S18	0	"11408528" and "11408669"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:14

S19	0	"11408528"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:14
S20	0	"11408528"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/27 21:15

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